It is hereby certified that the paper ID: IJRASET10219, entitled "Design of 4-bit Flash ADC using double tail comparator in 130nm technology" by Hemlata Gururani after review is found suitable and has been published in Volume 5, Issue IX, September 2017 in International Journal for Research in Applied Science & Engineering Technology.

Good luck for your future endeavors.

Editor in Chief, iJRASET
It is here by certified that the paper ID : IJRASET10219, entitled 
Design of 4-bit Flash ADC using double tail comparator in 130nm technology 
by 
Rachna Arya 

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