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Enhancement of Power Quality in Grid Network Using Compensator and Modified Power Filters

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Abstract: ***Now-a-days with the advancement in technology there is a drastic improvement in the semi-conductor devices. With this development and advantages, the semi-conductor devices got a permanent place in the power sector helping to ease the control of overall system. Moreover, most of the loads are also semi-conductor-based equipment. But the semi-conductor devices are non-linear in nature and draws non-linear current from the source. And also, the semi-conductor devices are involved in power conversion, which is either AC to DC or from DC to AC. This power conversion contains lot of switching operations which may introduce discontinuity in the current. Due to this discontinuity and non-linearity, harmonics are present which affect the quality of power delivered to the end user. To maintain the quality of power delivered, the harmonics should be filtered out. Thus, a device named Filter is used which serves this purpose.***
Keywords: Active Power Filter, Current Compensation, Total Harmonic Distortion,

I. INTRODUCTION

Electrical energy is the most efficient and widely used type of energy, and modern society is strongly reliant on it. Life would be impossible to envisage without the availability of power. At the same time, the quality of the electric power delivered is critical for the proper operation of end-user equipment. The word "power quality" has gained a lot of traction in the power industry, and it's something that both the electric power supply business and the end customers are worried about. The voltage and frequency ranges of the power determine the quality of power given to consumers. There are many filter topologies in the literature like- active, passive and hybrid. In this project the use of hybrid power filters for the improvement of electric power quality is studied and analysed.

Increased non-linearity results in a variety of unfavourable characteristics, including low system efficiency and a low power factor. It also causes annoyance to other customers and communication network interference in the area. Over the next few years, the impact of such non-linearity could be significant. As a result, overcoming these negative characteristics is critical.

Shunt passive filters, which are made up of tuned LC filters and/or high passive filters, are traditionally used to suppress harmonics and power capacitors are used to improve power factor. However, they have fixed compensation, are huge in bulk, and can exile resonance situations.

Active power filters are now seen as a viable alternative over the classical passive filters, to compensate harmonics and reactive power requirement of the non-linear loads. The objective of the active filtering is to solve these problems by combining with a much-reduced rating of the necessary passive components. [5].

II. SHUNT ACTIVE POWER FILTER

Figure 1 depicts a shunt active power filter diagram. At the Point of Common Coupling, a shunt active power filter is connected in parallel (PCC). PCC refers to the point where the source and load meet in the middle. In most cases, the active power filter is used.

The active power filter usually has an inverter structure, which can be either a voltage source inverter (VSI) or a current source inverter (CSI).

Because CSI has some limitations, most of them choose VSI-based shunt active power filters. A dc link capacitor is linked to the VSI's output terminal, acting as an energy storage element and utilized to maintain a constant DC voltage with small ripple in steady state. To obtain better correction, the capacitor's dc link voltage must be maintained constant. This is accomplished using a PI controller, which operates in a closed loop.

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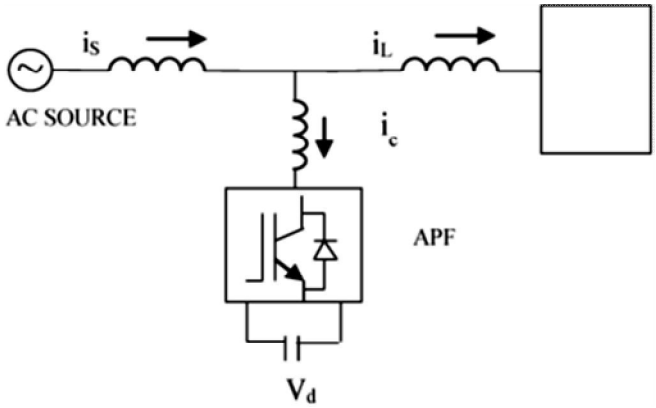


Fig 1: Shunt active power filter

III.MODELLING SHUNT ACTIVE POWER FILTER (SAPF)

Figure The filter is used to reduce the harmonics and improve the power quality. The filter that is connected to the system should be controlled effectively such that its response characteristics are as desired. Among the different available filter configurations, shunt active power filter is used in this work. The control circuit of the shunt connected APF is designed such way that the voltage injected by the APF compensates the harmonics and also enhances the performance of the shunt connected passive filter. The control strategy of the shunt active power filter is explained in detail below.

The instantaneous current i_s (t) can be written as,

$$i_s(t) = i_L(t) - i_c(t) \quad (1)$$

The source voltage V_s (t) is given by,

$$V_s(t) = v_m \sin \omega t \quad (2)$$

The load current will have a fundamental and harmonic component, if a non-linear load is applied, which can be represented as,

$$i_L(t) - \sum_{1-n}^{\infty} \int sin\ (\omega t + \Phi n)$$

$$= I_1\ sin(n\omega t + \Phi 1) + \sum_{n-2}^{\infty} \int sin\ (\omega t + \Phi n) \quad (3)$$

The instantaneous load power P_L (t) can be given as,

$$P_L(t) = V_s(t) * i_L(t)$$

$$= P_f(t) + P_r(t) + P_h(t) \quad (4)$$

The fundamental real power P_f (t) drawn by the load is,

$$P_f(t) = V_s(t) * i_s(t)$$

where, $i_s(t) = I_m \sin \omega t$ (5)

Source current supplied by the load after compensation is,

$$i_s(t) = P_f(t) / V_s(t) = I_1 \cos\Phi_1 \sin \omega t = I_m \sin \omega t \quad (6)$$

where, $I_{sm} = I_1 \cos \Phi_1$

There are also some switching losses I_{sl} in the PWM converter and hence the utility must supply a small overhead for the capacitor leakage and converter switching losses in addition to the real power of the load. The total peak current I_{sp} supplied by the source is,

$$I_{sp} = I_{sm} + I_{sl} \quad (7)$$

If the active power filter provides the total reactive power and harmonic power, then i_s (t) will be in phase with the source voltage and becomes sinusoidal. At this time the compensation current of the active power filter is,

$$i_c(t) = i_L(t) - i_s(t) \quad (8)$$

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The desired source current after compensation is,

$$i_{sa}^* = I_{sp} \sin \omega t$$
$$i_{sb}^* = I_{sp} \sin (\omega t - 120)$$
$$i_{sc}^* = I_{sp} \sin (\omega t + 120)$$

IV. SIMULATION PARAMETER

The MATLAB simulated model is shown in fig. 2. A model is developed to simulate the PI controller-based shunt active power filter in Simulink. The complete active power filter system is composed mainly of three-phase source, a nonlinear load, a voltage source PWM converter, and a PI controller. All these components are modelled separately, integrated and then solved to simulate the system. The parameters selected for simulation studies are given in table 1. The three phase source voltages are assumed to be balanced and sinusoidal.

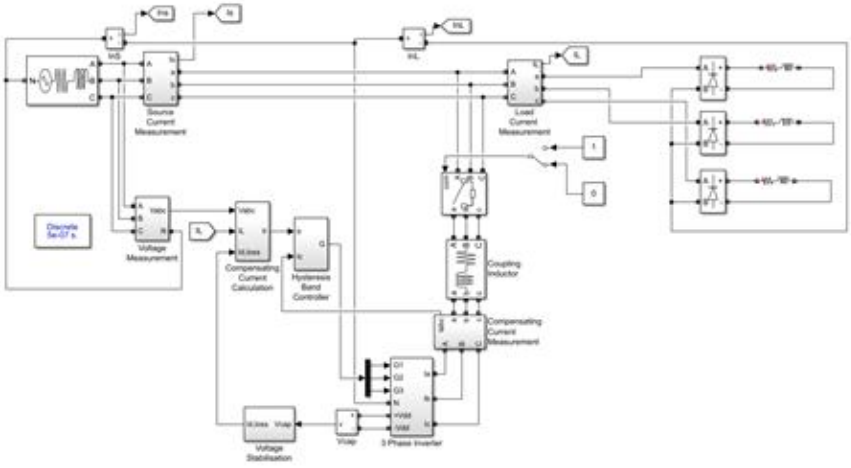


Fig. 2: MATLAB simulated model

A load with highly nonlinear characteristics is considered for the load compensation. The source current is equal to the load current when the compensator is not connected.

TABLE 1: System Parameters and Their Values.

System parameters	Values
Source voltage (V_s)	400V(peak)
System frequency (f)	50Hz
Source impedance (R_s, L_s)	0.1Ω;0.15mH
Filter impedance (R_c, L_c)	0.4Ω;3.35mH
Load impedance (R_l, L_l)	6.7Ω;20mH
DC link capacitance	2000μF
Reference DC link voltage (V_{dcref})	220V

From fig 3, It is clear that, the source current and source voltage are not in phase with each other. Due to this, the power factor of the system is reduced, and system becomes unstable. The change in system parameters such as voltage, current, and frequency caused by these complicated loads is referred to as "power quality concerns."

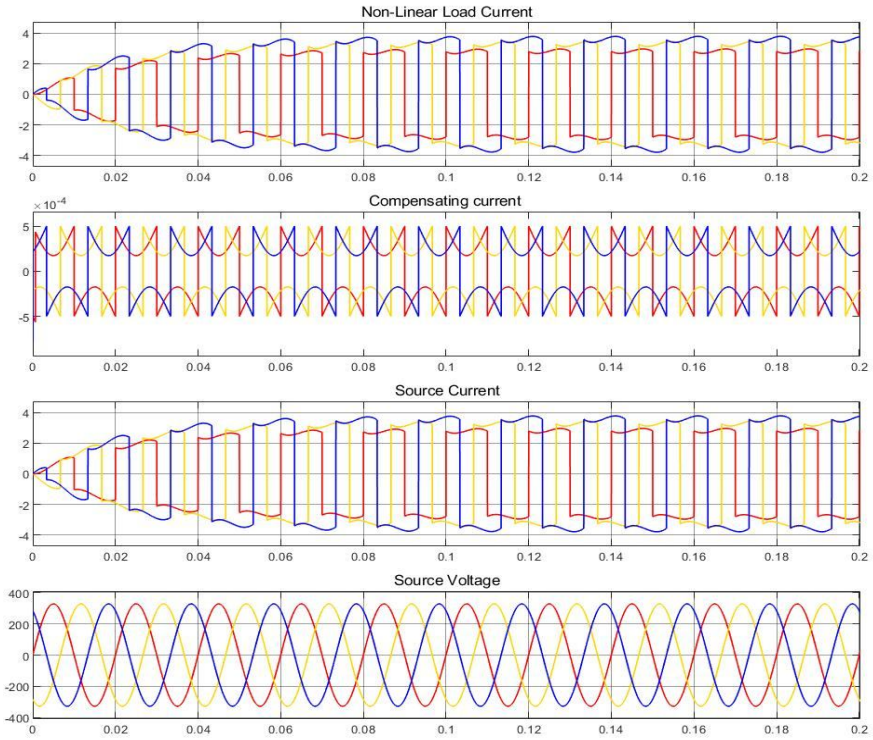


Fig 3: show the result of simulation of model before applying power filter to the system of non-linear load current, compensating current, source current and source voltage of phase R, Y and B respectively.

Poor power quality causes device and equipment failure, harmonics and unbalances in voltage and current, low power factor, and reactive power consumption. The major index for poor power quality is among these harmonics. As a result, it is vital to address these power quality issues and keep the % THD below specified limits, as dictated by IEEE standards.

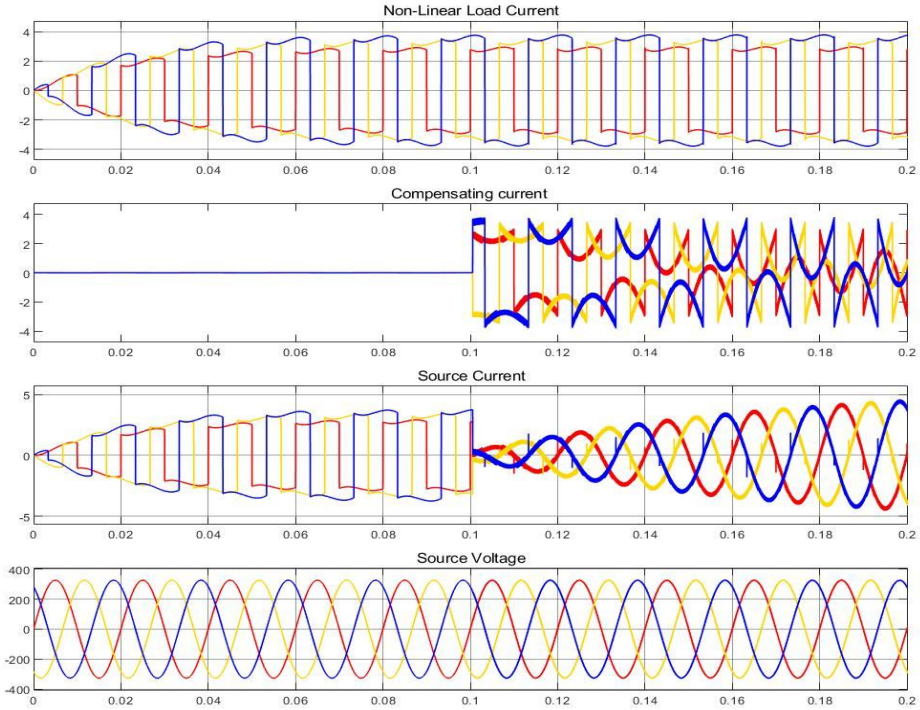


Fig 4: switching compensating current at t=0.1sec.

The compensator is switched ON at t=0.1s and the integral time square error (ITSE) performance index is used for optimizing the and coefficients of the PI controller. The optimum values are found to be 0.2 and 9.32, respectively, which corresponds to the minimum value of ITSE. The source currents as well as compensating currents for PI controllers are shown in fig 4.

From the wave forms it is clear that harmonic distortion is reduced after connecting compensator. The PI controller gives better harmonic compensation. From the responses it is depicted that the settling time required by the PI controller is approximately 10 cycles. In Fig 5 The THD of source current before switching Active Power Filter is shown. The number of cycles for calculating the percentage of THD of source current is 8 cycles selected from 0.2 sec. The THD of source current before applying Active power filter is 47.80%. The source voltage and source current are not in phase with each other. Due to this, the power factor is affected, and the system is not stable. Fig. 6 shows THD after applying the current compensation to the system. The settling time required for PI controller is approximately 10 cycles. The number of cycles for calculating the percentage of THD of source current is 8 cycles selected from 0.2 sec. The THD of source current after applying Active power filter is 3.18%. The source current becomes harmonic free after applying current compensation.

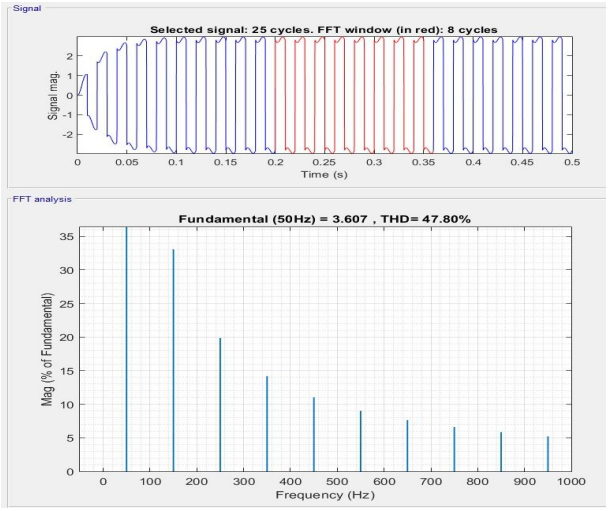


Fig 5: Total Harmonic Distortion (THD) before current compensation

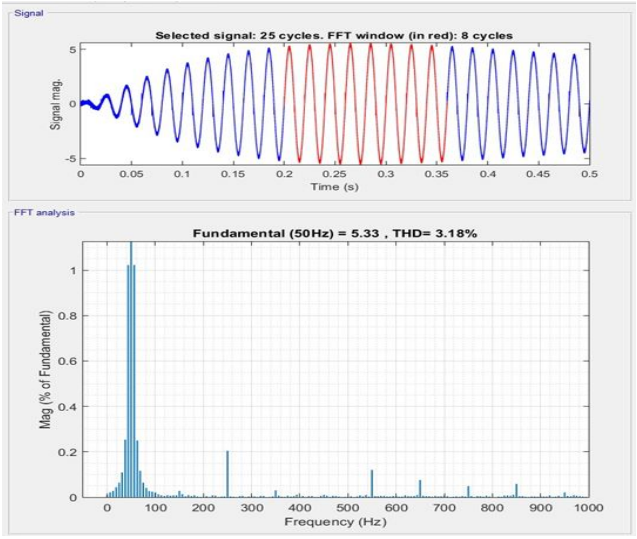


Fig 6: Total Harmonic Distortion (THD) after current compensation

The source current THD is reduced from 47.80% to 3.18% as shown in fig 5 and 6, the THD is reduced by 44.62%. After compensation both source voltage and current are in phase with each other means that the harmonics are eliminated, and reactive power is compensated to make power factor close to unity. As the source current is becoming sinusoidal after compensation power quality is improved.

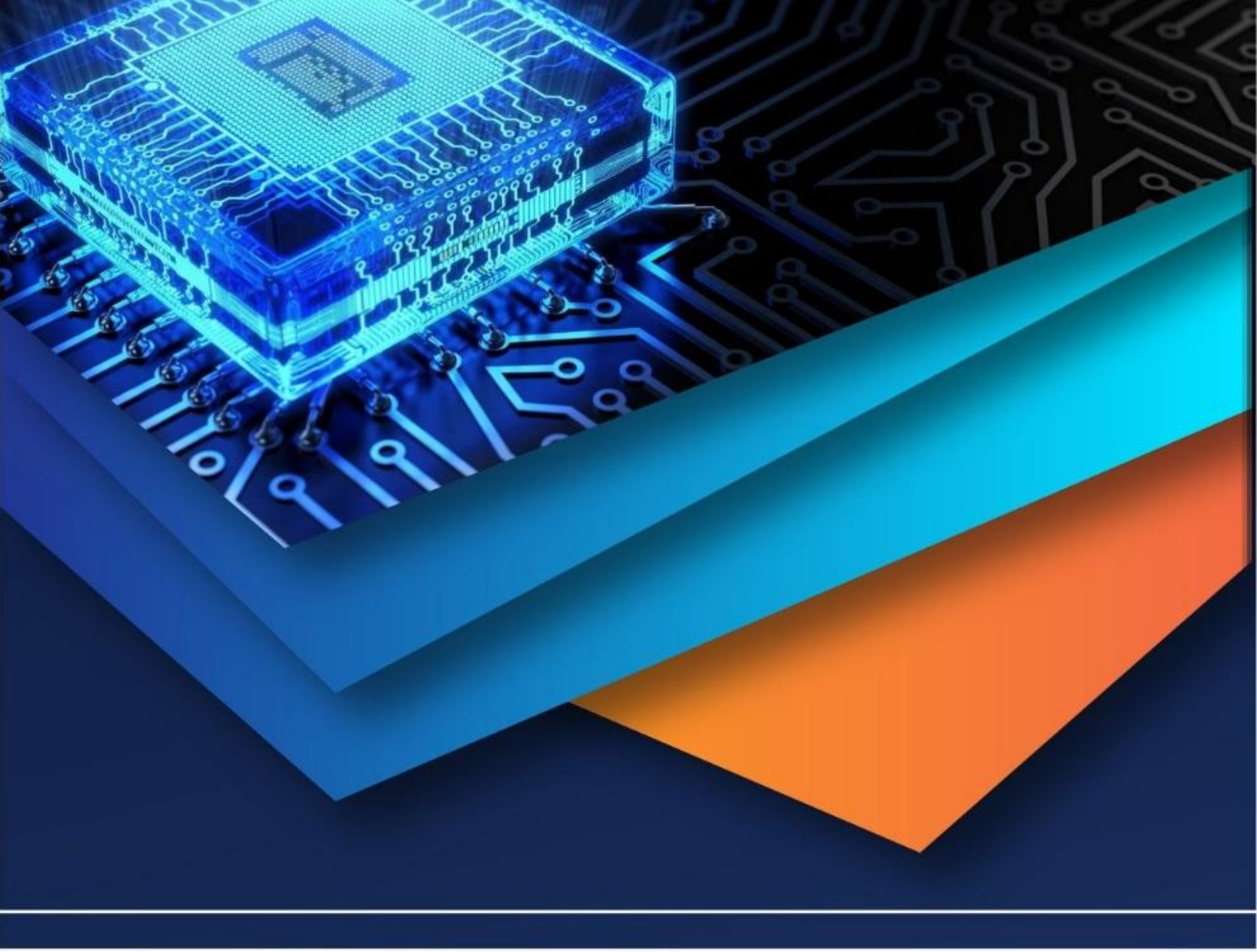
V. RESULT

A shunt active power filter has been investigated for power quality improvement. Various simulations are carried out to analyse the performance of the system. The PI controller-based Shunt active power filter is implemented for harmonic and reactive power compensation of the non-linear load. A program has been developed to simulate the PI controller-based shunt active power filter in MATLAB. It is found from simulation results that shunt active power filter improves power quality of the power system by eliminating harmonics and reactive current of the load current, which makes the load current sinusoidal and in phase with the source voltage. According to IEEE 519 standard, the maximum permissible THD for low voltage application is 5% and maximum permissible for individual voltage harmonics is 3%. The performance of the controllers has satisfied the IEEE standards as the resulted THD of source current after applying compensation is below permissible limit. A model has been developed in MATLAB SIMULINK and simulated to verify the results.

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