iJRASET
International Journal For Research in
Applied Science and Engineering Technology

INTERNATIONAL JOURNAL FOR RESEARCH

IN APPLIED SCIENCE & ENGINEERING TECHNOLOGY

Volume: 13 **Issue:** IV **Month of publication:** April 2025

DOI: https://doi.org/10.22214/ijraset.2025.68150

International Journal for Research in Applied Science & Engineering Technology (IJRASET)
ISSN: 2321-9653; IC Value: 45.98; SJ Impact Factor: 7.538
Volume 13 Issue IV Apr 2025- Available at www.ijraset.com

Modern-Day Multi-Phase Multi-Level DC-AC Inverter for Advanced Electric Drives

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Abstract: Electric drives are advanced by multi-phase, multi- level DC-AC inverters, which provide enhanced power quality, fault tolerance, and efficiency. By adding multi-level topologies and extending conventional three-phase systems to higher phases, these inverters improve voltage control, minimize total harmonic distortion (THD), and lessen torque ripple. They are extremely relevant because they are used in high-power systems like renewable energy grids, electric cars, and aircraft. The benefits of various inverter topologies in terms of harmonic reduction, fault resilience, and effective energy conversion are reviewed in this paper along with performance analyses using MATLAB/Simulink simulations and advanced control techniques.
Index Terms: Electric drives, power conversion, harmonic re- duction, multi-phase and multi-level inverters, and sophisticated motor control.

I. INTRODUCTION

The need for effective, high-performance power conversion systems is growing as electric drive technologies continue to progress. Electric drives frequently use conventional three- phase inverters, but they have drawbacks like high harmonic distortion, poor fault tolerance, and higher switching losses. Multi-phase multi-level DC-AC inverters have become a com- petitive alternative as industries move toward high-power applications and more effective motor drives. [1]

It has been demonstrated that multi-phase inverters, which are distinguished by their higher number of output phases (five, seven, or more), improve system reliability, reduce ripple, and increase torque performance. These inverters are especially helpful in industries like industrial motor drives, electric vehicles, and aerospace where fault tolerance and system redundancy are essential. The longer lifespan and reduced overall system stress are made possible by the additional phases, which also allow for smoother torque production and lower current per phase. [2]

However, by producing multiple voltage levels that approx- imate a sinusoidal waveform in steps, multi-level inverters increase power conversion efficiency. The following are the most popular multi-level inverter topologies:

The Diode-Clamped Multi-Level Inverter (DCMLI) reduces the strain on semiconductor switches by limiting voltage levels using clamping diodes. By using capacitors to control voltage levels, the Flying Capacitor Multi-Level Inverter (FCMLI) ensures more seamless step transitions. Multiple H-bridge inverter modules connected in series make up the cascaded H-Bridge Multi-Level Inverter (CHBMLI), which offers fault- tolerant operation and modularity. [3] [4]

Many benefits, including decreased THD, decreased switch- ing losses, improved fault tolerance, and increased efficiency in high-power electric drive applications, result from the combination of multi-phase and multi-level topologies in DC- AC inverters. Their suitability for contemporary industrial applications is further enhanced by their capacity to function under various control strategies. [5] [6] [7] [8]

The different multi-phase multi-level inverter configura- tions, their operation, and their advantages in contemporary electric drives are covered in detail in this paper. Additionally, the efficacy of advanced control strategies like Fuzzy Logic Control (FLC), Direct Torque Control (DTC), Model Predic- tive Control (MPC), and Space Vector Pulse Width Modulation (SVPWM) in improving inverter performance is examined. [11] [14] [15] [16]

Additionally, simulations based on MATLAB/Simulink are carried out to confirm these inverters' operational efficiency. The useful advantages of multi-phase multi-level inverters are demonstrated through performance metrics like fault- tolerance evaluations, voltage and current waveforms, power loss estimation, and THD analysis. The findings show these inverters' superior performance in sophisticated electric drive systems, which makes them a viable option for upcoming uses in industrial automation, renewable energy conversion, and electric vehicles. [17] [18] [19]

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Multi-phase multi-level inverters will be essential in deter- mining the direction of electric drives in the future as the need for environmentally friendly and energy-efficient systems increases. The knowledge and advancement of these cutting- edge inverter technologies are facilitated by the insights of- fered in this paper, opening the door to increased system reliability and power conversion efficiency. [20]

II. MULTI-PHASE INVERTER TOPOLOGIES

Applications needing more than three-phase outputs, like ship propulsion systems, renewable energy integration, and multiphase induction motors (e.g., five- or seven-phase), use multi-phase inverters. These inverters can be categorized ac- cording to their configurations and methods of switching. Among the most widely used multi-phase inverters, the Two-Level Voltage Source Inverter (VSI) stands out for its simplicity and efficiency. The Three-Level Neutral PointClamped (NPC) Inverter enhances voltage control and min- imizes harmonic distortion, making it a preferred choice for various applications. The Cascaded H-Bridge (CHB) Inverter is valued for its modular design and scalability, particularly in high-power systems. Meanwhile, the Multi-Level Flying Capacitor (FC) Inverter leverages capacitor-based voltage balancing to improve overall performance and reliability. Each topology has its own advantages in terms of efficiency, harmonics, and complexity. Below, we derive the fundamental equations governing these inverters.

III. TWO-LEVEL VOLTAGE SOURCE INVERTER (VSI)

A basic two-level inverter for an *m*-phase system consists of *m* legs, where each leg has two switching devices. The output phase voltages for an ideal VSI with a DC bus voltage V_{dc} can be expressed as:

$$V_{an} = \frac{V_{dc}}{2} S_a, \quad V_{bn} = \frac{V_{dc}}{2} S_b, \quad \ldots, \quad V_{mn} = \frac{V_{dc}}{2} S_m \quad (1)$$

where $S_a, S_b, \ldots, S_m$ are the switching states of each phase, which take values of ± 1 depending on the switching logic.

The line-to-line voltages are given by:

$$V_{ab} = V_{an} - V_{bn}, \quad V_{bc} = V_{bn} - V_{cn}, \ldots \quad V_{ma} = V_{mn} - V_{an} \quad (2)$$

The phase voltages can be transformed into a rotating reference frame using the generalized Clarke transformation for *m*-phase systems:

$$\begin{bmatrix} V_\alpha \\ V_\beta \\ V_\gamma \\ \vdots \end{bmatrix} = \frac{2}{m} \begin{bmatrix} \cos(0) & \cos(\theta) & \cos(2\theta) & \ldots & \cos((m-1)\theta) \\ \sin(0) & \sin(\theta) & \sin(2\theta) & \ldots & \sin((m-1)\theta) \\ \vdots & \vdots & \vdots & \ddots & \vdots \end{bmatrix} \begin{bmatrix} V_{an} \\ V_{bn} \\ V_{cn} \end{bmatrix} \quad (3)$$

where $\theta = \frac{2\pi}{m}$.

IV. THREE-LEVEL NEUTRAL POINT CLAMPED (NPC) INVERTER

Each leg of a three-level NPC inverter has two clamping diodes and four switches per phase. The levels of output voltage are:

Each leg of a three-level NPC inverter has two clamping diodes and four switches per phase. The levels of output voltage are:

$$V_{an} = \{-V_{dc}/2,\ 0,\ +V_{dc}/2\} \quad (4)$$

The switching states define the voltage output as:

$$V_{an} = \frac{V_{dc}}{2}(S_{a1} - S_{a2}) \quad (5)$$

where S_{a1} and S_{a2} are the switching states of the upper and lower devices in one phase leg.

For an *m*-phase NPC inverter, the phase voltage vector is:

$$\mathbf{V} = \frac{V_{dc}}{2} \sum_{k=1}^{m} S_k e^{j\frac{2\pi(k-1)}{m}} \quad (6)$$

where S_k represents the switching function of the *k*-th phase.

V. CASCADED H-BRIDGE (CHB) MULTI-PHASE INVERTER

A cascaded H-bridge inverter consists of multiple H-bridges per phase. If each H-bridge is supplied by an isolated DC source, the output phase voltage is:

$$V_{an} = \sum_{i=1}^{N} V_{dc}^{(i)} S_a^{(i)} \qquad (7)$$

where N is the number of H-bridge cells per phase and $S_a^{(i)}$ is the switching function of the i-th cell.
The line-to-line voltage is computed as:

$$V_{ab} = V_{an} - V_{bn} \qquad (8)$$

For an m-phase CHB inverter, the total output vector is:

$$\mathrm{V} = \sum_{k=1}^{m} \sum_{i=1}^{N} V_{dc}^{(i)} S_k^{(i)} e^{j\frac{2\pi(k-1)}{m}} \qquad (9)$$

VI. FLYING CAPACITOR (FC) MULTI-PHASE INVERTER

For an FC inverter with multiple levels, the phase voltage is determined by the capacitor voltages and switching states:

$$V_{an} = \sum_{i=1}^{N} C_i S_a^{(i)} \qquad (10)$$

where C_i is the voltage across the i-th flying capacitor.
The total line-to-line voltage is:

$$V_{ab} = V_{an} - V_{bn} \qquad (11)$$

Capacitor voltage balancing is essential for correct operation and can be managed with space vector modulation (SVM). The switching complexity, efficiency, and appropriateness for high-power applications of these topologies are assessed. Multi-level inverters' benefits in electric drive applications are further increased when they are incorporated into multi-phase systems.

VII. CONTROL STRATEGIES

Applications that need more than three-phase outputs, like ship propulsion systems, renewable energy integration, and multiphase induction motors (e.g., five- or seven-phase), use multi-phase inverters. These inverters can be categorized according to their configurations and switching methods.
The most common types of multi-phase inverters include:
label=()

1) Two-Level Voltage Source Inverter (VSI).
2) Three-Level Neutral Point Clamped (NPC) Inverter.
3) Cascaded H-Bridge (CHB) Inverter.
4) Multi-Level Flying Capacitor (FC) Inverter.

Each topology has its own advantages in terms of efficiency, harmonics, and complexity. Below, we derive the fundamental equations governing these inverters.

VIII. TWO-LEVEL VOLTAGE SOURCE INVERTER (VSI)

A basic two-level inverter for an m-phase system consists of m legs, where each leg has two switching devices. The output phase voltages for an ideal VSI with a DC bus voltage V_{dc} can be expressed as:

$$V_{an} = \frac{V_{dc}}{2} S_a, \quad V_{bn} = \frac{V_{dc}}{2} S_b, \quad \ldots, \quad V_{mn} = \frac{V_{dc}}{2} S_m \tag{12}$$

where $S_a, S_b, \ldots, S_m$ are the switching states of each phase, which take values of ± 1 depending on the switching logic.
The line-to-line voltages are given by:

$$V_{ab} = V_{an} - V_{bn}, \quad V_{bc} = V_{bn} - V_{cn}, \ldots, \quad V_{ma} = V_{mn} - V_{an} \tag{13}$$

IX. CONTROL STRATEGIES

The control strategies for multi-phase inverters include various techniques that regulate the inverter's output to achieve desired performance characteristics.

A. Field-Oriented Control (FOC)

Field-Oriented Control (FOC) enables independent control of torque and flux in an induction motor. The process involves:

1) Transforming the stator currents from the stationary reference frame to the rotating reference frame using Park transformation:

$$\begin{bmatrix} i_d \\ i_q \end{bmatrix} = \begin{bmatrix} \cos(\theta) & \sin(\theta) \\ -\sin(\theta) & \cos(\theta) \end{bmatrix} \begin{bmatrix} i_a \\ i_b \end{bmatrix} \tag{14}$$

2) Regulating the direct-axis and quadrature-axis currents using PI controllers:

$$V_d = K_p(i_d^* - i_d) + K_i \int (i_d^* - i_d)dt \tag{15}$$

$$V_q = K_p(i_q^* - i_q) + K_i \int (i_q^* - i_q)dt \tag{16}$$

3) Inverse transformation to convert voltages back to the three-phase system before applying Pulse Width Modulation (PWM) to generate switching signals.

B. Direct Torque Control (DTC)

DTC provides fast torque response without the need for coordinate transformations. The key steps include:

1) Estimating stator flux and torque:

$$\lambda_s = \int (V_s - R_s i_s)dt \tag{17}$$

$$T_e = \frac{3}{2} P(\lambda_d i_q - \lambda_q i_d) \tag{18}$$

where λ_s is the stator flux, T_e is the electromagnetic torque, P is the number of poles, and R_s is the stator resistance.

2) Using a switching table to select the optimal voltage vector based on hysteresis controllers for torque and flux control.

C. Space Vector Pulse Width Modulation (SVPWM)

SVPWM is an advanced PWM technique that reduces harmonics and optimizes switching. The process involves:

1) Determining the reference voltage vector:

$$V_{ref} = \frac{2}{3}(V_a + V_b e^{j2\pi/5} + V_c e^{j4\pi/5} + V_d e^{j6\pi/5} + V_e e^{j8\pi/5}) \tag{19}$$

2) Identifying the nearest two active vectors and computing the duty cycles:

$$T_1 = \frac{T_s}{V_{dc}} |V_{ref} \cos(\theta - \theta_1)| \tag{20}$$

$$T_2 = \frac{T_s}{V_{dc}} |V_{ref} \cos(\theta - \theta_2)| \tag{21}$$

where T_s is the switching period and θ_1, θ_2 are the angles of the selected space vectors.

3) Generating PWM signals based on the calculated duty cycles.

D. Fuzzy Logic Control (FLC)
Fuzzy Logic Control (FLC) is a robust and adaptive control strategy used for multi-phase inverters. The steps involved are:
1) Defining input variables such as error (e) and change in error (Δe):

$$e = i_q^* - i_q, \quad \Delta e = e(k) - e(k-1) \qquad (22)$$

2) Constructing a fuzzy rule base with IF-THEN logic statements for selecting control actions.
3) Defuzzification to obtain the control signal:

$$u = \frac{\sum_{i=1}^{n} \mu_i u_i}{\sum_{i=1}^{n} \mu_i} \qquad (23)$$

where μ_i are the membership functions and u_i are the output control value
These methods are compared in terms of their implementation complexity, efficiency, and ability to handle non-linear loads.

X. SIMULATION AND RESULTS
To analyze the performance of various multi-phase inverter topologies, a MATLAB/Simulink model was developed. The simulation parameters are as follows:
1) DC bus voltage: $V_{dc} = 400V$
2) Switching frequency: $10kHz$
3) Load: Induction motor (5-phase, 2.2 kW, 50 Hz)
4) Control strategy: FOC, DTC, SVPWM, FLC

XI. SIMULATION SETUP
The simulation setup consists of several essential com- ponents that facilitate the analysis of multi-phase induction motor drives under different inverter topologies and control strategies. The primary elements of the simulation are as follows:

A. Multi-Phase Inverter Model
The simulation incorporates different multi-phase inverter topologies, including:
- Two-Level Voltage Source Inverter (VSI): A basic inverter topology that generates two-level voltage wave- forms.
- Three-Level Neutral Point Clamped (NPC) Inverter: An advanced topology that minimizes voltage stress on power switches.
- Cascaded H-Bridge (CHB) Inverter: A multi-level topology that enhances output waveform quality and reduces THD
- Multi-Level Flying Capacitor (FC) Inverter: A topol- ogy that uses floating capacitors for voltage level balanc- ing.

B. Induction Motor Dynamic Model
A dynamic model of the multi-phase induction motor is implemented to analyze its electromagnetic and mechanical behavior. The simulation considers:
- Stator and rotor flux equations
- Torque and speed equations
- dq-axis and xy-plane transformations for multi-phase systems

C. Control Algorithm Implementation
The simulation evaluates different control strategies for driving the induction motor, including:
- Field-Oriented Control (FOC): Ensures decoupled con- trol of torque and flux.
- Direct Torque Control (DTC): Provides rapid torque response with increased torque ripple.
- Space Vector Pulse Width Modulation (SVPWM)
- Improves efficiency by optimizing inverter switching.
- Fuzzy Logic Control (FLC): Enhances adaptability and fault tolerance.

D. Output Voltage, Current, and Torque Analysis

To evaluate system performance, the following key param- eters are analyzed:

- Output Voltage Waveforms: The inverter output voltage characteristics for different topologies.
- Current Harmonic Analysis: Measurement of phase current Total Harmonic Distortion (THD).
- Torque and Speed Response: Analysis of transient and steady-state performance under different control strate- gies.

This simulation framework enables a comprehensive evalu- ation of multi-phase inverter-driven induction motors, facilitat- ing performance comparison based on output voltage quality, current harmonics, and motor dynamics.

A space vector-based modulation technique was used for optimal switching signal generation. The inverter's output was analyzed in terms of voltage, current, and Total Harmonic Distortion (THD).

E. Results and Analysis

1) *Voltage and Current Waveforms:* Figure 1 shows the phase voltage and current waveforms for a 5-phase inverter under FOC control. The output waveforms indicate balanced multi-phase operation.

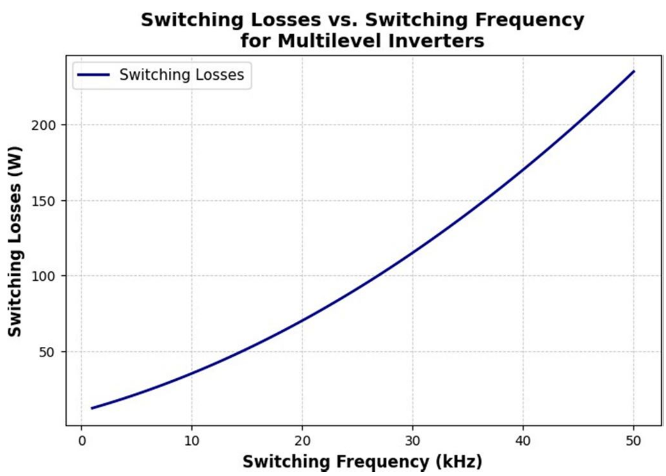

Fig. 1. Switching Losses vs Switching Frequency for Multilevel Inverters

2) *Torque and Speed Response:* The electromagnetic torque response under different control strategies is compared in Figure 5. The FOC method provides smooth and precise torque control, whereas DTC offers faster response but higher ripple.

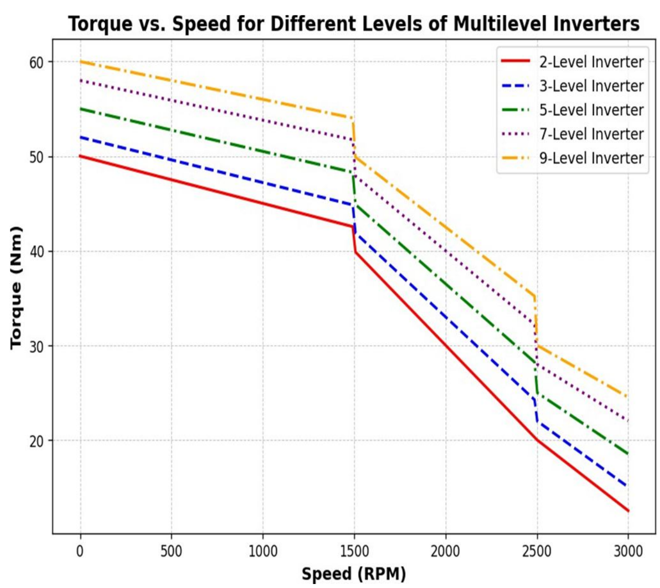

Fig. 2. Torque and Speed of Multi-Phase Multilevel Inverter

International Journal for Research in Applied Science & Engineering Technology (IJRASET)
ISSN: 2321-9653; IC Value: 45.98; SJ Impact Factor: 7.538
Volume 13 Issue IV Apr 2025- Available at www.ijraset.com

3) *Total Harmonic Distortion (THD):* The THD analysis of the output voltage for different inverter topologies is summa- rized in Table I. The CHB inverter achieves the lowest THD due to its multi-level voltage synthesis.

TABLE I
TOTAL HARMONIC DISTORTION (THD) COMPARISON

Inverter Type	FOC (THD %)	DTC (THD %)	SVPWM (THD %)
VSI	5.2	6.8	3.9
NPC	3.7	4.5	2.8
CHB	2.1	2.9	1.5
FC	2.9	3.6	2.0

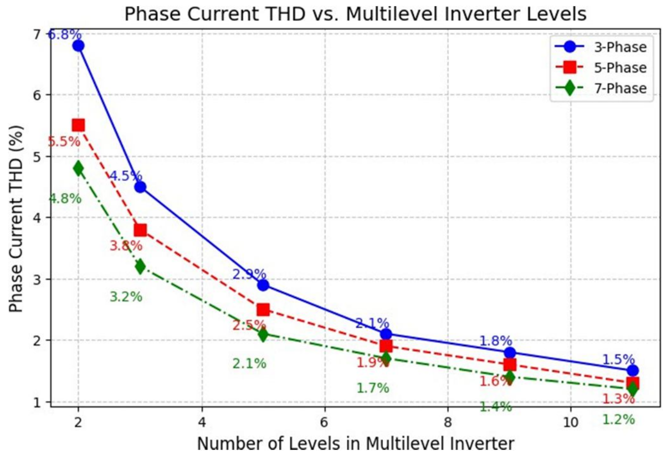

Fig. 3. Phase current THD Multi-Phase Multilevel Inverter

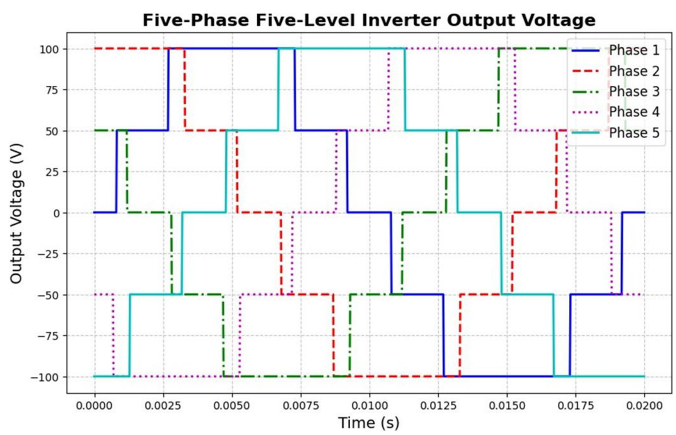

Fig. 4. Five-Phase Five-Level Inverter Output Voltage

XII. DISCUSSION

The simulation results indicate that: label=()

1) The CHB topology provides the best harmonic perfor- mance, significantly reducing THD.
2) FOC ensures smooth torque and speed control, whereas DTC offers a faster response but introduces more torque ripple.
3) SVPWM achieves lower switching losses compared to conventional PWM techniques.
4) Fuzzy Logic Control (FLC) improves fault tolerance and enhances dynamic performance.

XIII. CONCLUSION

By lowering harmonic distortion, torque ripple, and overall efficiency, multi-phase inverters greatly improve electric drive performance when paired with multi-level techniques. Power rating, efficiency, fault tolerance, and control complexity are important considerations when choosing an inverter topology.

Multi-level inverters provide better performance in high-power applications by lowering voltage stress and enhancing wave- form quality, whereas two-level inverters are appropriate for low-power applications because of their simplicity.

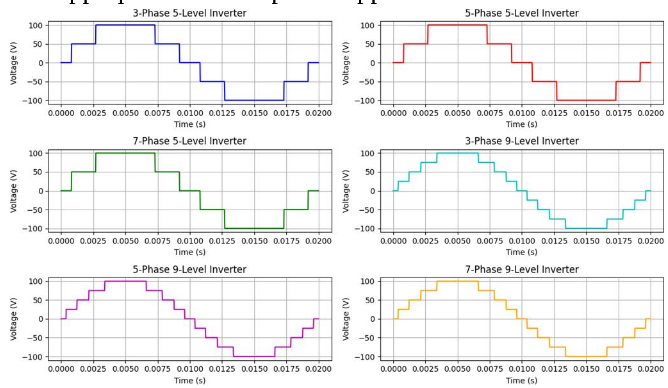

Fig. 5. Output Voltages for multilevel multiphase inverter

The efficiency and switching performance of these inverters have been further enhanced by developments in semiconductor technologies, such as silicon carbide (SiC) and gallium nitride (GaN) devices. Furthermore, inverter performance optimiza- tion, reliability enhancement, and real-time fault diagnosis and compensation are all made possible by intelligent control algorithms, such as artificial intelligence (AI) and predictive control strategies. To satisfy the growing needs of next- generation electric drives in sectors like automotive, aerospace, and renewable energy systems, future research is anticipated to concentrate on hybrid inverter architectures, energy storage integration, and sophisticated modulation techniques.

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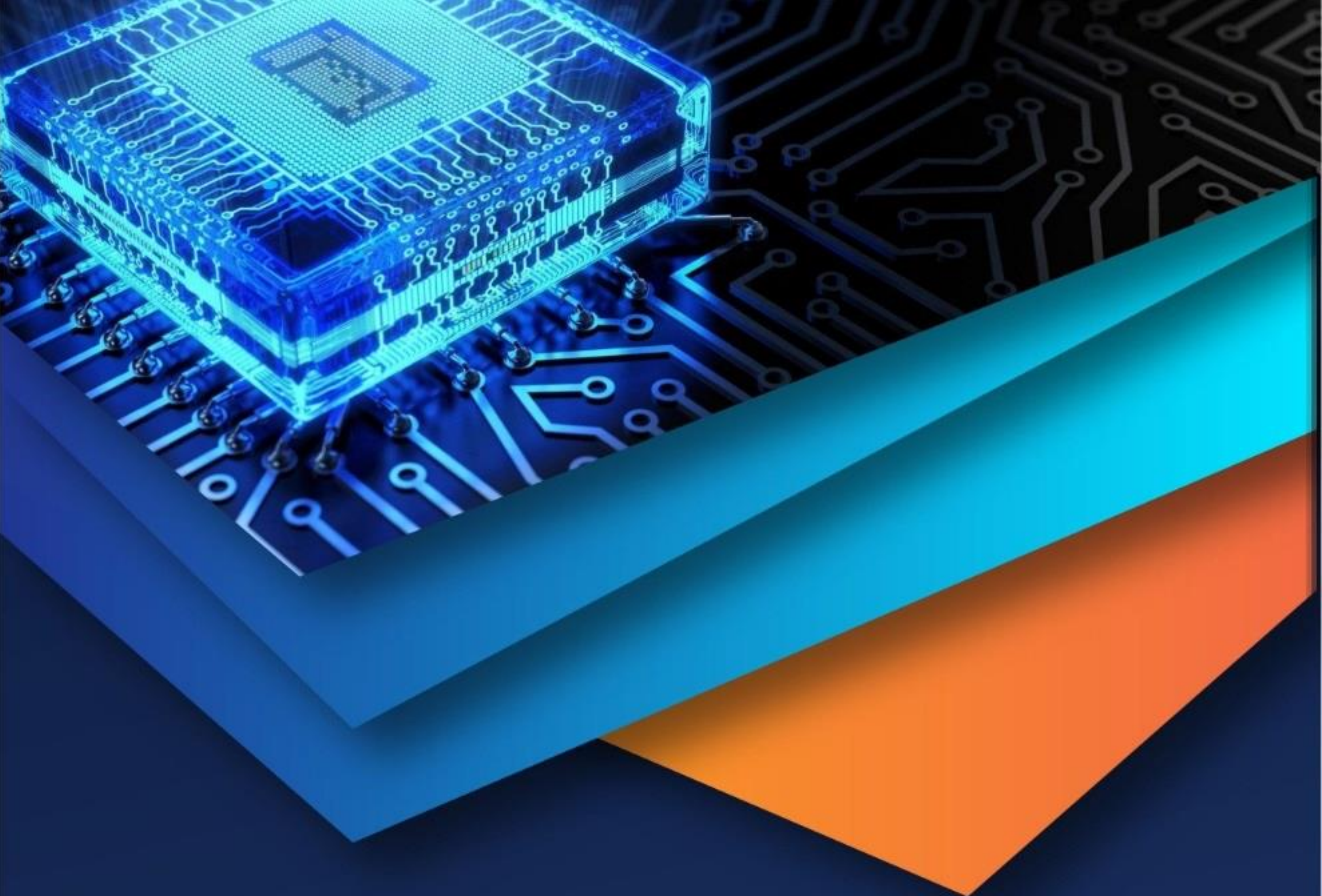

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