



IN APPLIED SCIENCE & ENGINEERING TECHNOLOGY

Volume: 8 Issue: VIII Month of publication: August 2020 DOI: https://doi.org/10.22214/ijraset.2020.31115

www.ijraset.com

Call: 🕥 08813907089 🔰 E-mail ID: ijraset@gmail.com



Design and Analysis of CPAT for Unified Power Flow Controller Application

Mohammed Asaduddin¹, N Mahesh Babu²

¹M.Tech Scholar, ²Associate Professor, Department of Electrical & Electronics Engineering, Bheema Institute of Technology & Science, Adoni, Kurnool Dist, A.P, India

Abstract: The reliability, power quality, line congestion, lines capacity and stability are major issues in transmission and distribution systems for efficient power transmission. The UPFC will provide solutions for these issues. In this paper, we have proposed a Custom Power Active Transformer (CPAT) based unified power flow controller (UPFC) for power flow control and compensation. This proposed system has been design using MATLAB SIMULINK tool. The performance of CPAT's and its capability for providing various UPFC services are investigated. The simulations results have confirmed that it provides the required grid services efficiently.

Keywords: Unified Power Flow Controller, Grid, custom power active transformers, Magnetic circuits, Power control, converter, shunt converter, series converter.

I. INTRODUCTION

There were many obstacles, and technological issues posed by the growing demand for distributed generation to enable substantial grid contributions. Since of the erratic conduct of renewable generation and the ever-increasing need for electrical energy, substation building and operation has undergone many innovations to overcome these challenges[1]. To ensure a efficient, sustainable and smart electric network, integration of monitoring and control functionalities across the power grid, has evolved to meet these demands [2]. Such functionalities were commissioned via power electronic converters which proved to have several beneficial impacts on the distribution network [3-5] and the transmission network [6-8].

Flexible AC Transmission Systems (FACTS) have demonstrated their ability to provide services to support power transmission and power distribution systems effectively and to improve their power system efficiency, consistency and its stability [9]. Of these power systems, the UPFC system is considered to be the most flexible tool for reducing line congestion and increasing the efficiency of existing transmission lines. Connection of power electronics converters system to offer UPFC services has been achieved either through large isolation transformer system, complex multilevel topologies or back to back converters system that handle the rated line power [10-12]. Transformer-less solution involving multilevel topologies emerge from the need to remove bulky isolation transformers system requirement. However, the full rated voltage is needed for multilevel topologies and for this a complex system configuration is required. The transformers system provides isolation and they can be used to connect both shunt and series power devices to power system very efficiently. However, when considering high power compensation systems, size, cost and footprint are a further concern. To resolve these concerns, the incorporation of electronic control devices into a traditional transformer system has been observed in recent literature aimed at the use of off-the-shelf converters system [13-15] or the development of a transformer system based on control electronics [16]. These solutions, however, have either addressed compensation of one type [13], specific applications [14-15] or involve higher power and complex architectures [16]. The CPAT system of monolithic transformer core structure has been proposed and investigated in [17] and [18] and this CPAT integrates the series and shunt power electronics converters to the distribution transformer. The Sen Transformer has be proposed and investigated in [15] and is comparable with CPAT system when combining multiple transformers into the single unit. The shunt services offered by CPAT to power system are compensation of reactive power, elimination of harmonics and mitigation of inrush current in grid. But, the Sen Transformer cannot provide some of these services. But, investigation of CPAT system for single phase applications is carried out and also CPAT system investigation as Unified Power Quality Conditioner (UPQC) for power distribution network.

On the basis of operation principle of a CPAT system the power transmission applications can be realized as it offers auxiliary windings and can be utilized for any of the shunt-series application. Various researchers across the world have been investigated the several methods to resolve the isolation requirement of the power system with the use of transformer-less techniques [19-20] and also they investigated the power electronics based transformers system [10]. Such a system configurations are utilized in several power transmission applications [21-24] and the bulky line transformers system requirement can be avoided for isolation and there



is creation of challenge in the complexity in the maintenance and design of the power electronics system [25]. Since, bulky power transformers system are an key element in a power network for matching the voltage levels between the different buses, the CPAT system incorporates both series and shunt transformers within these power transformers system. In this case an isolated UPFC system can be built by replacing any power transformer system with a CPAT system using fractional power converters system. Thus, this offers an integrated UPFC system inside any power transformer system.

This paper is organized as follows: in section II the related work is presented. Section III presents the proposed CPAT-UPFC and its design, the simulation results is presented in section IV. Finally, conclusions are summarized in section V.

II. RELATED WORK

H Lee et al [18] 2019 have proposed the novel topology of UPFC with N: 2 transformers. They implemented the 3-phase UPFC using 3-single phase transformers. This system is employed with auto transformer to reduce the power and voltage rating of transformers and switches. The benefits offered by this proposed system are installation spaces and reduction in costs.

M.A. Elsaharty et al [19] in 2018 have investigated the use of power electronics integrated transformer (PEIT) for compensation of power distribution system. They investigated the CPAT system control for unified power-quality controller (UPQC) application to compensate for requirements of reactive power, and mitigate the grid system inrush current. Also, they investigated the control of CPAT system for attenuation of grid current and load voltage harmonics. The CPAT merits and its performance are validated via simulation tool and experimental implementation.

P. Li et al [20] in 2017 have investigated the feasibility for application of MMC-UPFC system in the power grid with 500kv Suzhou power network, in the china. They carried out the simulation to verify and validate the effect on the power flow and voltage regulation and investigated the use of UPFC for regulating can avoid the overload of key transmission section in winter season under heavy load, condition.

Y. Liu, et al [21] in 2016 has carried out the investigation about transformer-less UPFC system for interconnection of two synchronous AC grids system with larger phase difference. This system is constructed from two cascaded multilevel inverters system. With the phase difference determination, we can find real power flow, among two generators

F. Z. Peng et al [22] in 2016 have carried out the design and analysis of transformer-less UPFC system, and this system is constructed using cascaded multilevel inverter system. This proposed system offers benefits like transformer-less, less weight system, offers higher efficiency, higher reliability and lost cost. Such a system is suited for wind power, and solar power transmission.

S. Yang et al [23] in 2016 have design and simulation of transformer-less UPFC system and carried out its major operation such as modulation and control. This proposed system can be installed to obtain maximize energy and optimize energy in grids system, transmission congestion can be reduced and also it can enable high penetration of renewable energy sources.

H. Zhengyu et al[24] in 2000 have investigated the use of UPFC system in the designing of power systems and its modeling, interface and control strategy are investigated to verify the effectiveness and also carried out the case study. They proposed the system with new power frequency model and its performance is improved significantly and fairly power flow control.

III. PROPOSED SYSTEM

A. Proposed CPAT for UPFC

We propose a three phase CPAT called Power Electronics Integrated Transformer based UPFC system. This system can be used to regulate the power flow between primary winding and secondary winding. Also it can be used to compensate the reactive power and for elimination of harmonics in the grid. The figure 1 illustrates the configuration of 3-phase CPAT system and this useful in power transmission system applications. This configuration is constructed using 3-single phase CPATs with grid connected to its primary and secondary windings and 3-phase converter (back to back) is connected to shunt and series windings. This converter will control the shunt winding current and series winding voltage.



Figure 1 Three-phase CPAT configuration



The function of shunt converter is to eliminate the harmonic and compensates the reactive power to primary windings. Further, the function of series converter is to control the active and reactive powers through secondary winding. This proposed system is useful in controlling the flow of power among two striff grids. This CPAT system can be designed and analysed to investigate its performance.

B. Proposed System Design

The complete CPAT UPFC were designed using MATLAB-SIMULINK is as shown in figure 2 through figure 4. Its circuits were design using the blocks available in SIMULINK tool. Every block of diagram is taken from libraries and drawn the complete diagram and interconnections are made with the specification. Then the design is saved and it has been further simulated to find out flow of various current, voltage and power.



Figure 2 the CPAT configuration



Figure 3 the UPFC power measurement

International Journal for Research in Applied Science & Engineering Technology (IJRASET)



ISSN: 2321-9653; IC Value: 45.98; SJ Impact Factor: 7.429 Volume 8 Issue VIII Aug 2020- Available at www.ijraset.com



Figure 4 the power flow control

The figure 2 indicates the design of configuration of CPAT and design of UPFC power measurement is shown in figure 3. Further the figure 4 shows the circuit design of power flow control.

IV. SIMULATION RESULTS

The simulation results of designed CPAT system are shown in terms of snapshots of execution results. To validate the effectiveness of proposed system the simulation analysis were carried out on SIMULINK platform with the use of system parameters like nominal grid voltage of 500KV, normal system frequency of 50kHz, and nominal DC link power of 200MW.

The figure 5 through figure 8 is the simulated results indicating the primary and secondary current of shunt converter under enabled and disabled mode. Also, it indicates the analysis of harmonics spectrum.



Figure 5: shunt converter in enabled mode (a) Primary current waveform (b) Harmonics spectrum analysis





Figure 6: shunt converter in enabled mode (a) Secondary current waveform (b) Harmonics spectrum analysis



(b) Harmonics spectrum analysis

The figure 9 and figure 10 illustrates the waveform of ip1 and ip1 current and its harmonics spectrum analysis graph for all controllers under enabled mode.





Figure 8: shunt converter in disabled mode (a) Secondary current waveform (b) harmonics spectrum analysis



The Figure 7 and Figure 8 illustrates the primary current consists of 3rd, 5th and 7th-order harmonics due to no power flow in this scenario, the primary current consists of magnetizing current of CPAT and DC bus regulation current.





Figure 10: All controllers in enabled mode (a) ip4 current waveform (b) Harmonics spectrum

V. CONCLUSION AND FUTURE WORK

The CPAT-UPFC system has been successfully modelled, analysed and simulated using SIMULINK tool and the control architecture has been evaluated to investigate the CPAT system ability to be function as a UPFC for power distribution systems. The performance analysis and simulation results have been confirmed the effectiveness of CPAT-UPFC system in eliminating grid system harmonic currents and compensation of reactive power of distribution system. Also, it has been verified and validated the control of power flow between two stiff grids system. Hence, this system is efficient for power distribution system application.

Future research work is to design a smart power system using UPFC with low cost equipments and reconfigurable architectures. This system can be analyzed and experimental validated. Also we can develop the UPFC control algorithms.

REFERENCES

- M. S. Mahmoud, M. S. Rahman and F. M. A. L. Sunni, "Review of microgrid architectures a system of systems perspective," IET Renewable Power Gen., vol. 9, no. 8, pp. 1064-1078, 2015.
- [2] Q. Huang, S. Jing, J. Li, D. Cai, J. Wu and W. Zhen, "Smart Substation: State of the Art and Future Development," IEEE Trans. Power Del., vol. 32, no. 2, pp. 1098-1105, Apr. 2017.
- [3] H. Liao and J. V. Milanović, "On capability of different FACTS devices to mitigate a range of power quality phenomena," IET Generation, Transmission & Distribution, vol. 11, no. 5, pp. 1202-1211, Mar. 2017.
- [4] M. Shahparasti, M. Mohamadian, P. T. Baboli and A. Yazdianp, "Toward Power Quality Management in Hybrid AC-DC Microgrid Using LTC-L Utility Interactive Inverter: Load Voltage-Grid Current Tradeoff," IEEE Trans. Smart Grid, vol. 8, no. 2, pp. 857-867, Mar. 2017.
- [5] J. Barr and R. Majumder, "Integration of Distributed Generation in the Volt/VAR Management System for Active Distribution Networks," IEEE Trans. Smart Grid, vol. 6, no. 2, pp. 576-586, Mar. 2015.
- [6] M. A. Sayed and T. Takeshita, "All Nodes Voltage Regulation and Line Loss Minimization in Loop Distribution Systems Using UPFC," IEEE Trans. Power Electron., vol. 26, no. 6, pp. 1694-1703, June 2011.
- [7] F. Z. Peng, "Flexible AC Transmission Systems and Resilient AC Distribution Systems in Smart Grid," Proc. IEEE, vol. 105, no. 11, pp. 2099-2115, Nov. 2017.
- [8] E. Rakhshani, D. Remon, A. M. Cantarellas, J. M. Garcia and P. Rodriguez, "Virtual Synchronous Power Strategy for Multiple HVDC Interconnections of Multi-Area AGC Power Systems," IEEE Trans. Power Syst., vol. 32, no. 3, pp. 1665-1677, May 2017.
- [9] W. Litzenberger, K. Mitsch and M. Bhuiyan, "When It's Time to Upgrade: HVDC and FACTS Renovation in the Western Power System," IEEE Power Energy Mag., vol. 14, no. 2, pp. 32-41, Mar. 2016.
- [10] M. Andresen, K. Ma, G. De Carne, G. Buticchi, F. Blaabjerg and M. Liserre, "Thermal Stress Analysis of Medium-Voltage Converters for Smart Transformers," IEEE Trans. Power Electron., vol. 32, no. 6, pp. 4753-4765, Jun. 2017.
- [11] S. Yang, Y. Liu, X. Wang, D. Gunasekaran, U. Karki and F. Z. Peng, "Modulation and Control of Transformerless UPFC," IEEE Trans. Power Electron., vol. 31, no. 2, pp. 1050-1063, Feb. 2016.

International Journal for Research in Applied Science & Engineering Technology (IJRASET)



ISSN: 2321-9653; IC Value: 45.98; SJ Impact Factor: 7.429

Volume 8 Issue VIII Aug 2020- Available at www.ijraset.com

- [12] C. Liang et al., "Harmonic Elimination Using Parallel Delta-Connected Filtering Windings for Converter Transformers in HVDC Systems," IEEE Trans. Power Del., vol. 32, no. 2, pp. 933-941, Apr. 2017.
- [13] C. Wang, X. Yin, Z. Zhang and M. Wen, "A Novel Compensation Technology of Static Synchronous Compensator Integrated With Distribution Transformer," IEEE Trans. Power Del., vol. 28, no. 2, pp. 1032-1039, Apr. 2013.
- [14] B. B. Ambati and V. Khadkikar, "Variable Frequency Transformer Configuration for Decoupled Active-Reactive Powers Transfer Control," IEEE Trans. on Energy Convers., vol. 31, no. 3, pp. 906-914, Sept. 2016.
- [15] J. Liu and V. Dinavahi, "Nonlinear Magnetic Equivalent Circuit-Based Real-Time Sen Transformer Electromagnetic Transient Model on FPGA for HIL Emulation," IEEE Trans. Power Del., vol. 31, no. 6, pp. 2483-2493, Dec. 2016.
- [16] L. F Costa, G. D Carne, G. Buticchi and M. Liserre, "The Smart Transformer: A solid-state transformer tailored to provide ancillary services to the distribution grid," IEEE Power Electron. Mag., vol. 4, no. 2, pp. 56-67, June 2017.
- [17] M.A. Elsaharty, J. Rocabert, I. Candela and P. Rodriguez, "Three-Phase Custom Power Active Transformer for Power Flow Control Applications," IEEE Trans. Power Electron., Early Access, June 2018.
- [18] Hyun-Jun Lee, Dae-Shik Lee and Young-Doo Yoon "Unified Power Flow Controller Based on Autotransformer Structure," ectronics 2019, 8, 1542; pp:1-15
- [19] M.A. Elsaharty, J.I. Candela and P. Rodriguez, "Power System Compensation Using a Power Electronics Integrated Transformer", IEEE Trans. Power Del., vol. 33, no. 4, pp. 1744-1754, Aug. 2018.
- [20] P. Li, Y. Wang, C. Feng and J. Lin, "Application of MMC-UPFC in the 500 kV power grid of Suzhou," The Journal of Engineering, vol. 2017, no. 13, pp. 2514-2518, 2017.
- [21] Y. Liu, S. Yang, X. Wang, D. Gunasekaran, U. Karki and F. Z. Peng, "Application of Transformer-Less UPFC for Interconnecting Two Synchronous AC Grids With Large Phase Difference," IEEE Trans. Power Electron., vol. 31, no. 9, pp. 6092-6103, Sept. 2015
- [22] F. Z. Peng, Y. Liu, S. Yang, S. Zhang, D. Gunasekaran and U. Karki, "Transformer-Less Unified Power-Flow Controller Using the Cascade Multilevel Inverter," IEEE Trans. Power Electron., vol. 31, no. 8, pp. 5461-5472, Aug. 2016.
- [23] S. Yang, Y. Liu, X. Wang, D. Gunasekaran, U. Karki and F. Z. Peng, "Modulation and Control of Transformerless UPFC," IEEE Trans. Power Electron., vol. 31, no. 2, pp. 1050-1063, Feb. 2016.
- [24] H. Zhengyu et al., "Application of unified power flow controller in interconnected power systems-modeling, interface, control strategy, and case study," IEEE Trans. Power Syst., vol. 15, no. 2, pp. 817–824, May 2000.
- [25] M. Andresen, K. Ma, G. D Carne, G. Buticchi, F. Blaabjerg and M. Liserre, "Thermal Stress Analysis of Medium-Voltage Converters for Smart Transformers," IEEE Trans. Power Electron., vol. 32, no. 6, pp. 4753-4765, Jun. 2017.











45.98



IMPACT FACTOR: 7.129







INTERNATIONAL JOURNAL FOR RESEARCH

IN APPLIED SCIENCE & ENGINEERING TECHNOLOGY

Call : 08813907089 🕓 (24*7 Support on Whatsapp)