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### INTERNATIONAL JOURNAL FOR RESEARCH IN APPLIED SCIENCE AND ENGINEERING TECHNOLOGY (IJRASET)

## FPGA Implementation OF RFID Mutual Authentication Protocol

Chetna Devi Kh<sup>1</sup>, Mrs.Ashwini<sup>2</sup>

PG Scholar at PESIT Bangalore<sup>1</sup>, Asst. Prof, Dept of E&C PESIT, Bangalore<sup>2</sup>

Abstract: Radio frequency identification, or RFID, is a generic term for technologies that use radio waves to automatically identify people or objects. The wide deployment of RFID also incurs many security concerns and practical attacks. Many authentication schemes have been proposed. But these schemes have not been verified with hardware implementation. This project focuses on solving the security problem related to RFID tag reader. A specially designed pad generation (PadGen) function will be included to improve security. The PadGen function is used to produce a cover-coding pad to mask the tag's access password before the data are transmitted . The optimization of the area utilization is also done . The hardware implementation of the project is done using FPGA kit. Simulation of the codes is done with the help of XILINX and ModelSim.

#### Keywords-RFID, FPGA, Mutual authentication, Security.

#### **I.INTRODUCTION**

Due to the low cost and the convenience in identifying an object without physical contact, Radio Frequency Identification (RFID) systems have become more and more popular. It uses a wireless system that can provide enterprises with efficient real-time product track-and-trace capability. An RFID application contains three basic roles: tag, reader and back-end database. Each tag contains a unique identification, often called the tag identification (TID). The reader is used to query the tag's TID and forward it to the back-end database. Once the tag is found valid, the back-end database will look up its product information for further processing. RFID tags are classified into three types: active, semi-passive, and passive.

The wide deployment of RFID also incurs many security concerns and practical attacks . The ISO 18000-6C protocol, also known as the EPC C1G2 protocol, provides basic security tools using a 16-b pseudorandom number generator (PRNG) and a 16-b cyclic redundancy code (CRC).

An authentication scheme was originally included as part of the EPC C1G2 industrial standard to secure RFID transactions. However, the Gen2 specification has the vulnerability that the TID is transmitted without any guard.Solving the authentication protocol with the area optimisation is very important.

#### **II.LITERATURE SURVEY**

Till now papers are published based on the security requirements and detailed study of challenges . A clear survey was done for recent technical research on the problems of privacy and security for radio frequency identification(RFID.) A detailed analysis of the security weakness of the one-way reader-to-tag authentication scheme has been provided.

#### III.PROPOSED SYSTEM

The proposed system aims at solving the authentication problem. The RFID tag-reader mutual authentication (TRMA) scheme will be utilised to solve the problem. A

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protocol using the XOR operation tag's Apwd and Kpwd for achieving a TRMA scheme. Designing new authentication protocols conforming to the standard and providing an adequate security level are therefore exciting challenges.

In the authentication scheme based on the ISO 18000-6C protocol the RFID systems are vulnerable to cloning attacks as well as password disclosure and information leakage .To solve this problem PadGen function is proposed. The PadGen function is the key component in constructing the

16-b pads to cover-code the two 16-b Apwd halves ApwdM (comprising the 16 most significant bits) and ApwdL (comprising the 16 least significant bits). The PadGen function is used to produce a cover-coding pad to mask the tag's Apwd before transmission. PadGen takes two 16-b random numbers  $RT_x$  and  $RM_x$  as its inputs.

#### **IV.WORK METHODOLOGY**

The proposed scheme based on XOR or MOD operation to generate PadGen function is an improvement over the weak one-way reader-to-tag authentication scheme proposed by the 18000-6C protocol.





Xor scheme is utilised because it does not require the implementation of any special cryptographic hash functions/keys within the tag and a center server/database.



Fig 2: Functional block diagram of the xor-padgen operation

The present approach will obtain  $PAD_1$  and  $PAD_2$  using one set of  $(R_{Tx}, R_{Mx})$ . As compared with the design, the present approach is an efficient way to generate PAD function for mutual authentication.

#### V.RESULT

Simulations of the proposed design will be conducted in the Xilinx 12.2 and modelsim. The verified Verilog code will be then downloaded on FPGA.



Fig 3: Simulation result using Modelsim

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Fig 4: Verilog simulation result using FPGA downloaded data

 Table I

 FPGA resource utilizations of the proposed design

Resources		Xor scheme		
		Propos	Previous work	
		ed		
		work		
.Xilinx	Number	416	599	
12.2	of slice			
Vertex5	registers			
XC5VLX3	Number	330	427	
0	of slice			
	LUTs			

#### VI. CONCLUSION

The PadGen functions are used to protect the Apwd against exposure. The main advantage of the proposed scheme is that it does not require the implementation of any special cryptographic hash functions/keys within the tag and a center server/database. There is also no need for the tag and the reader to synchronize security keys/hash values. The PadGen function was modified to strengthen the security of the mutual authentication scheme in this project. Area can also be effectively reduced in the process. In future design can be still made robust by implementing search based mechanism to improve the recolonization time.

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