



ISSN No. : 2321-9653

# IJRASET

**International Journal for Research in Applied  
Science & Engineering Technology**

IJRASET is indexed with Crossref for DOI-DOI : 10.22214

Website : [www.ijraset.com](http://www.ijraset.com), E-mail : [ijraset@gmail.com](mailto:ijraset@gmail.com)

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Factor: 7.429



45.98  
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THOMSON REUTERS  
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## Certificate

*It is here by certified that the paper ID : IJRASET10732, entitled  
Design Analysis of Charge-Pump Phase Locked Loop with Analogy Lock Signal  
Generator*

*by  
Fahad Faisal*

*after review is found suitable and has been published in  
Volume 5, Issue XI, November 2017*

*in  
International Journal for Research in Applied Science &  
Engineering Technology*

*(International Peer Reviewed and Refereed Journal)*

*Good luck for your future endeavors*

*By [Signature]*

Editor in Chief, IJRASET