It is hereby certified that the paper ID: IJRASET13684, entitled "Reliable Low Power Multiplier Design Using Reduced Precision Redundancy by Wallace Architecture" by P. Lakshmi Neeraja after review is found suitable and has been published in Volume 6, Issue II, February 2018 in International Journal for Research in Applied Science & Engineering Technology.

Good luck for your future endeavors.

Editor in Chief, iJRASET
It is hereby certified that the paper ID: IJRASET13684, entitled **Reliable Low Power Multiplier Design Using Reduced Precision Redundancy by Wallace Architecture**

by **Ch. Rajesh Babu**

after review is found suitable and has been published in **Volume 6, Issue II, February 2018** in **International Journal for Research in Applied Science & Engineering Technology**

**Good luck for your future endeavors**

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**Editor in Chief, iJRASET**