It is hereby certified that the paper ID: IJRASET16487, entitled "VLSI Implementation of a Reverse Converter for a Class of Six Moduli Set" by M. Hemalatha after review is found suitable and has been published in Volume 6, Issue IV, April 2018 in International Journal for Research in Applied Science & Engineering Technology.

Good luck for your future endeavors.

Editor in Chief, IJRASET
It is hereby certified that the paper ID: IJRASET16487, entitled
VLSI Implementation of a Reverse Converter for a Class of Six Moduli
Set
by
J. Sangavi
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