It is hereby certified that the paper ID: IJRASET16596, entitled

**Design and Implementation of a Low Power Vedic Multiplier**

by

**Neethu Johny**

after review is found suitable and has been published in

**Volume 6, Issue IV, April 2018**

in

**International Journal for Research in Applied Science & Engineering Technology**

Good luck for your future endeavors

Editor in Chief, iJRASET
It is hereby certified that the paper ID: IJRASET16596, entitled "Design and Implementation of a Low Power Vedic Multiplier" by Mayur S. M., after review is found suitable and has been published in Volume 6, Issue IV, April 2018 in International Journal for Research in Applied Science & Engineering Technology.

Good luck for your future endeavors.

Editor in Chief, iJRASET