It is here by certified that the paper ID: IJRASET18099, entitled **Design of a Low Power Vedic Multiplier using BKG Reversible Logic Gate** by **Mayur S M** after review is found suitable and has been published in **Volume 6, Issue VI, June 2018** in **International Journal for Research in Applied Science & Engineering Technology**

Good luck for your future endeavors

Editor in Chief, iJRASET
It is hereby certified that the paper ID: IJRASET18099, entitled "Design of a Low Power Vedic Multiplier using BKG Reversible Logic Gate" by Neethu Johny after review is found suitable and has been published in Volume 6, Issue VI, June 2018 in International Journal for Research in Applied Science & Engineering Technology. Good luck for your future endeavors.

Editor in Chief, iJRASET