

ISSN No. : 2321-9653

# URASET

### International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI : 10.22214

Website : www.ijraset.com, E-mail : ijraset@gmail.com



It is here by certified that the paper ID : IJRASET30290, entitled

Design of High Speed Sequence Detector using Verilog

by Bejagam Divya



ISRA Journal Impact Factor: **7.429** 





THOMSON REUTERS Researcher ID: N-9681-2016



TOGETHER WE REACH THE GOAL SJIF 7.429

after review is found suitable and has been published in Volume 8, Issue VII, July 2020

in

were

Editor in Chief, **iJRASET** 

International Journal for Research in Applied Science & Engineering Technology (International Peer Reviewed and Refereed Journal) Good luck for your future endeavors



ISSN No. : 2321-9653

# URASET

### International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI : 10.22214

Website : www.ijraset.com, E-mail : ijraset@gmail.com



JIF

ISRA Journal Impact Factor: **7.429** 





THOMSON REUTERS Researcher ID: N-9681-2016



TOGETHER WE REACH THE GOAL SJIF 7.429

It is here by certified that the paper ID : IJRASET30290, entitled Design of High Speed Sequence Detector using Verilog

> by Somashekar Malipatil

after review is found suitable and has been published in Volume 8, Issue VII, July 2020 in

were

Editor in Chief, **iJRASET** 

International Journal for Research in Applied Science & Engineering Technology (International Peer Reviewed and Refereed Journal) Good luck for your future endeavors



ISSN No. : 2321-9653

# URASET

### International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI : 10.22214

Website : www.ijraset.com, E-mail : ijraset@gmail.com

### Certificate

JISRA F

ISRA Journal Impact Factor: **7.429** 





THOMSON REUTERS Researcher ID: N-9681-2016



TOGETHER WE REACH THE GOAL SJIF 7.429

It is here by certified that the paper ID : IJRASET30290, entitled Design of High Speed Sequence Detector using Verilog

> by R. Madhuri Muddapu

after review is found suitable and has been published in Volume 8, Issue VII, July 2020

in

were

Editor in Chief, **iJRASET** 

International Journal for Research in Applied Science & Engineering Technology (International Peer Reviewed and Refereed Journal) Good luck for your future endeavors