



ISSN No. : 2321-9653

iJRASET

International Journal for Research in Applied
Science & Engineering Technology

iJRASET is indexed with Crossref for DOI-DOI : 10.22214

Website : www.ijraset.com, E-mail : ijraset@gmail.com

Certificate

It is here by certified that the paper ID : IJRASET337, entitled

*Efficient Design of Power and Clock network for Large Chip Design Using Energy
Recycling Technique*

by

Saravana kumar

*after review is found suitable and has been published in
Volume 2, Issue IV, April 2014
in*

*International Journal for Research in Applied Science &
Engineering Technology
(International Peer Reviewed and Refereed Journal)
Good luck for your future endeavors*

By [Signature]

Editor in Chief, iJRASET

 ISRA

ISRA Journal Impact
Factor: 7.429



45.98

INDEX COPERNICUS



THOMSON REUTERS
Researcher ID: N-9681-2016



TOGETHER WE REACH THE GOAL
SJIF 7.429



ISSN No. : 2321-9653

iJRASET

International Journal for Research in Applied
Science & Engineering Technology

iJRASET is indexed with Crossref for DOI-DOI : 10.22214

Website : www.ijraset.com, E-mail : ijraset@gmail.com

Certificate

It is here by certified that the paper ID : IJRASET337, entitled

*Efficient Design of Power and Clock network for Large Chip Design Using Energy
Recycling Technique*

by

Shafiq Mansoor

*after review is found suitable and has been published in
Volume 2, Issue IV, April 2014*

in

*International Journal for Research in Applied Science &
Engineering Technology
(International Peer Reviewed and Refereed Journal)
Good luck for your future endeavors*

By [Signature]

Editor in Chief, iJRASET

J^oSRA
I
F

ISRA Journal Impact
Factor: 7.429

INDEX COPERNICUS



THOMSON REUTERS
Researcher ID: N-9681-2016

doi 10.22214/IJRASET
cross ref



TOGETHER WE REACH THE GOAL
SJIF 7.429



ISSN No. : 2321-9653

iJRASET

International Journal for Research in Applied
Science & Engineering Technology

iJRASET is indexed with Crossref for DOI-DOI : 10.22214

Website : www.ijraset.com, E-mail : ijraset@gmail.com

Certificate

It is here by certified that the paper ID : IJRASET337, entitled

*Efficient Design of Power and Clock network for Large Chip Design Using Energy
Recycling Technique*

by

Rajarajan Last Name

*after review is found suitable and has been published in
Volume 2, Issue IV, April 2014*

in

*International Journal for Research in Applied Science &
Engineering Technology
(International Peer Reviewed and Refereed Journal)
Good luck for your future endeavors*

By [Signature]

Editor in Chief, iJRASET

JISRA
JIF

ISRA Journal Impact
Factor: 7.429

INDEX COPERNICUS



THOMSON REUTERS
Researcher ID: N-9681-2016

TOGETHER WE REACH THE GOAL
SJRIF 7.429



ISSN No. : 2321-9653

iJRASET

International Journal for Research in Applied
Science & Engineering Technology

iJRASET is indexed with Crossref for DOI-DOI : 10.22214

Website : www.ijraset.com, E-mail : ijraset@gmail.com

Certificate

It is here by certified that the paper ID : IJRASET337, entitled

*Efficient Design of Power and Clock network for Large Chip Design Using Energy
Recycling Technique*

by

Sumitha Last Name

*after review is found suitable and has been published in
Volume 2, Issue IV, April 2014*

in

*International Journal for Research in Applied Science &
Engineering Technology
(International Peer Reviewed and Refereed Journal)
Good luck for your future endeavors*

By [Signature]

Editor in Chief, iJRASET

JISRA
JOURNAL
IMPACT
FACTOR

ISRA Journal Impact
Factor: **7.429**



45.98
INDEX COPERNICUS



THOMSON REUTERS
Researcher ID: N-9681-2016



doi 10.22214/IJRASET
cross ref



TOGETHER WE REACH THE GOAL
SJIF 7.429