

JRASET!

International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI: 10.22214

Website: www.ijraset.com, E-mail: ijraset@gmail.com



It is here by certified that the paper ID: IJRASET4159, entitled

Balanced Pipeline Stages with Minimum Logic Delay on Encrypted High Speed Data Using FPGA Based AES Algorithm

> by Sowmyaa. S

after review is found suitable and has been published in Volume 4, Issue III, March 2016

in

International Journal for Research in Applied Science & Engineering Technology
(International Peer Reviewed and Refereed Journal)
Good luck for your future endeavors



ISRA Journal Impact Factor: **7.429**









Py Live Editor in Chief, IJRASET



JRASET!

International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI: 10.22214

Website: www.ijraset.com, E-mail: ijraset@gmail.com



It is here by certified that the paper ID: IJRASET4159, entitled

Balanced Pipeline Stages with Minimum Logic Delay on Encrypted High Speed Data Using FPGA Based AES Algorithm

> by N. Jayanthi

after review is found suitable and has been published in Volume 4, Issue III, March 2016

in

International Journal for Research in Applied Science & Engineering Technology
(International Peer Reviewed and Refereed Journal)
Good luck for your future endeavors



ISRA Journal Impact Factor: **7.429**









By were

Editor in Chief, iJRASET