



ISSN No. : 2321-9653

# IJRASET

**International Journal for Research in Applied  
Science & Engineering Technology**

IJRASET is indexed with Crossref for DOI-DOI : 10.22214

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*It is here by certified that the paper ID : IJRASET6726, entitled  
An Efficient Reused VLSI Architecture of FMO/Manchester Encoding using SOLS Technique  
for DSRC Applications*

*by*

*I. V. Rameswar Reddy*

*after review is found suitable and has been published in  
Volume 5, Issue III, March 2017*

*in*

*International Journal for Research in Applied Science &  
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Volume 5, Issue III, March 2017*

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