

RASET

International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI: 10.22214

Website: www.ijraset.com, E-mail: ijraset@gmail.com



It is here by certified that the paper ID: IJRASET59217, entitled

Design and Implementation of 32 Bit Linear Feedback Shift Register using FPGA

by Sajal Tripathi

after review is found suitable and has been published in Volume 12, Issue V, May 2024

in

International Journal for Research in Applied Science & Engineering Technology
(International Peer Reviewed and Refereed Journal)
Good luck for your future endeavors



ISRA Journal Impact Factor: **7.429**









By were



JRASET

International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI: 10.22214

Website: www.ijraset.com, E-mail: ijraset@gmail.com



It is here by certified that the paper ID: IJRASET59217, entitled

Design and Implementation of 32 Bit Linear Feedback Shift Register using FPGA

by Sharad Gupta

after review is found suitable and has been published in Volume 12, Issue V, May 2024

in

International Journal for Research in Applied Science & Engineering Technology
(International Peer Reviewed and Refereed Journal)
Good luck for your future endeavors



ISRA Journal Impact Factor: **7.429**









By were



JRASET!

International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI: 10.22214

Website: www.ijraset.com, E-mail: ijraset@gmail.com



It is here by certified that the paper ID: IJRASET59217, entitled

Design and Implementation of 32 Bit Linear Feedback Shift Register using FPGA

by Shivanshu Pratap Singh

after review is found suitable and has been published in Volume 12, Issue V, May 2024

in

International Journal for Research in Applied Science &
Engineering Technology
(International Peer Reviewed and Refereed Journal)
Good luck for your future endeavors



ISRA Journal Impact Factor: **7.429**









By were



RASET

International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI: 10.22214

Website: www.ijraset.com, E-mail: ijraset@gmail.com



It is here by certified that the paper ID: IJRASET59217, entitled

Design and Implementation of 32 Bit Linear Feedback Shift Register using FPGA

by Kamal Bhatia

after review is found suitable and has been published in Volume 12, Issue V, May 2024

in

International Journal for Research in Applied Science &
Engineering Technology
(International Peer Reviewed and Refereed Journal)
Good luck for your future endeavors



ISRA Journal Impact Factor: **7.429**









By were