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## Certificate

*It is here by certified that the paper ID : IJRASET66410, entitled  
A Comparison of FPGA Implementation of Latency-Based Solvers for Power  
Electronic System Real-Time Simulation*

*by  
Abdulmajid Mohammed Ali Matouq*

*after review is found suitable and has been published in  
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*Good luck for your future endeavors*

*By*

Editor in Chief, IJRASET



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