Certificate

It is here by certified that the paper ID : IJRASET44628, entitled

Column Bypassing Multiplier Implementation on FPGA

by

Arun A V

after review is found suitable and has been published in

Volume 10, Issue VI, June 2022

_in_

International Journal for Research in Applied Science & Engineering Technology

(International Peer Reviewed and Refereed Journal)

Good luck for your future endeavors

Editor in Chief, iJRASET

Certificate

It is here by certified that the paper ID : IJRASET44628, entitled

Column Bypassing Multiplier Implementation on FPGA

_by_

Adam Asif S

after review is found suitable and has been published in

Volume 10, Issue VI, June 2022

in

International Journal for Research in Applied Science &
Engineering Technology

(International Peer Reviewed and Refereed Journal)

Good luck for your future endeavors

Editor in Chief, iJRASET

Certificate

It is here by certified that the paper ID : IJRASET44628, entitled

Column Bypassing Multiplier Implementation on FPGA

_by_

Aravindh R

after review is found suitable and has been published in

Volume 10, Issue VI, June 2022

_in_

International Journal for Research in Applied Science &

Engineering Technology

(International Peer Reviewed and Refereed Journal)

Good luck for your future endeavors

Editor in Chief, iJRASET

Certificate

It is here by certified that the paper ID : IJRASET44628, entitled

Column Bypassing Multiplier Implementation on FPGA

_by_

Balachandran G

after review is found suitable and has been published in
Volume 10, Issue VI, June 2022

_in_

International Journal for Research in Applied Science &
Engineering Technology
(International Peer Reviewed and Refereed Journal)
Good luck for your future endeavors

Editor in Chief, iJRASET