

ISSN No. : 2321-9653

URASET

International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI : 10.22214

Website : www.ijraset.com, E-mail : ijraset@gmail.com



J J F

ISRA Journal Impact Factor: **7.429**





THOMSON REUTERS Researcher ID: N-9681-2016





It is here by certified that the paper ID : IJRASET39742, entitled Design and Implementation of Floating-Point Addition and Floating-Point Multiplication

> by Nagireddy Kavya

after review is found suitable and has been published in Volume 10, Issue I, January 2022 in

were

Editor in Chief, **iJRASET**

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