

ISSN No.: 2321-9653

RASET

International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI: 10.22214

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It is here by certified that the paper ID: IJRASET41478, entitled

Design and Implementation of UART Using FPGA Board

by Anchal Govil

after review is found suitable and has been published in Volume 10, Issue IV, April 2022

in

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