

ISSN No. : 2321-9653



International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI : 10.22214

Website : www.ijraset.com, E-mail : ijraset@gmail.com



JISRA F

ISRA Journal Impact Factor: **7.429** 





THOMSON REUTERS Researcher ID: N-9681-2016



TOGETHER WE REACH THE GOAL SJIF 7.429

It is here by certified that the paper ID : IJRASET72917, entitled Fault Diagnosis and Redundant Technique for 24 Hours Clock Design Using VHDL

by Ms. K. Pavani

after review is found suitable and has been published in

Volume 13, Issue VII, July 2025 in



Editor in Chief, **iJRASET** 

International Journal for Research in Applied Science & Engineering Technology (International Peer Reviewed and Refereed Journal) Good luck for your future endeavors



ISSN No. : 2321-9653



International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI : 10.22214

Website : www.ijraset.com, E-mail : ijraset@gmail.com



JIF

ISRA Journal Impact Factor: **7.429** 





THOMSON REUTERS Researcher ID: N-9681-2016



TOGETHER WE REACH THE GOAL SJIF 7.429

It is here by certified that the paper ID : IJRASET72917, entitled Fault Diagnosis and Redundant Technique for 24 Hours Clock Design Using VHDL

> by Mr. Kurada V B C Sekara <mark>R</mark>ao

after review is found suitable and has been published in Volume 13, Issue VII, July 2025

in

were

Editor in Chief, **iJRASET** 

International Journal for Research in Applied Science & Engineering Technology (International Peer Reviewed and Refereed Journal) Good luck for your future endeavors



ISSN No. : 2321-9653



International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI : 10.22214

Website : www.ijraset.com, E-mail : ijraset@gmail.com



JISRA F

ISRA Journal Impact Factor: **7.429** 





Researcher ID: N-9681-2016



TOGETHER WE REACH THE GOAL SJIF 7.429

It is here by certified that the paper ID : IJRASET72917, entitled Fault Diagnosis and Redundant Technique for 24 Hours Clock Design Using VHDL

> by Dr. B. S. Rao

after review is found suitable and has been published in

Volume 13, Issue VII, July 2025 in

g were

Editor in Chief, **iJRASET** 

International Journal for Research in Applied Science & Engineering Technology (International Peer Reviewed and Refereed Journal) Good luck for your future endeavors