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*It is here by certified that the paper ID : IJRASET47245, entitled  
High-Speed Hybrid Logic Full Adder Using High-Performance 10-T XOR-XNOR Cell  
Using 18-nm FinFET Technology*

*by*

*Ms. K S Indrani*

*after review is found suitable and has been published in  
Volume 10, Issue X, October 2022*

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