

JRASET

International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI: 10.22214

Website: www.ijraset.com, E-mail: ijraset@gmail.com



It is here by certified that the paper ID: IJRASET46100, entitled

Implementation of 2-bit Multiplier Circuit Using Pass Transistor Logic

by Panchadi Swathi

after review is found suitable and has been published in Volume 10, Issue VII, July 2022

in

International Journal for Research in Applied Science &
Engineering Technology
(International Peer Reviewed and Refereed Journal)
Good luck for your future endeavors



ISRA Journal Impact Factor: **7.429**









By were

Editor in Chief, iJRASET



International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI: 10.22214

Website: www.ijraset.com, E-mail: ijraset@gmail.com



It is here by certified that the paper ID: IJRASET46100, entitled

Implementation of 2-bit Multiplier Circuit Using Pass Transistor Logic

Gudla Bhanu Gupta

after review is found suitable and has been published in Volume 10, Issue VII, July 2022

in

International Journal for Research in Applied Science & Engineering Technology (International Peer Reviewed and Refereed Journal)

Good luck for your future endeavors



ISRA Journal Impact Factor: 7.429









Editor in Chief, iJRASET