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## Certificate

*It is here by certified that the paper ID : IJRASET66841, entitled  
Power Reduction in Advanced Technology Nodes Using Multi-Bit Flip-Flops and  
Banking Techniques in Physical Design*

*by  
Selva Lakshman Murali*

*after review is found suitable and has been published in  
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*Good luck for your future endeavors*

*By [Signature]*

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